FEB 10 2005

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Docket No. 219713US2/ims

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Shigeru KAWANAKA

SERIAL NO: 10/075,464 GAU: 2826

FILED: February 15, 2002 EXAMINER: Kevin V. QUINTO

FOR: SEMICONDUCTOR MEMORY DEVICE AND ITS MANUFACTURING METHOD

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

Applicant(s) wish to disclose the following information.

REFERENCES

■ The applicant(s) wish to make of record the references cited in the attached Korean and Chinese Office Actions and listed on the attached form PTO-1449. Copies of the listed references are attached, where required, as are either statements of relevancy or any readily available English translations of pertinent portions of any non-English language references.

☐ A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

RELATED CASES

☐ Attached is a list of applicant's pending application(s), published application(s) or issued patent(s) which may be related to the present application. In accordance with the waiver of 37 CFR 1.98 dated September 21, 2004, copies of the cited pending applications are not provided. Cited published and/or issued patents, if any, are listed on the attached PTO form 1449.

☐ A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

CERTIFICATION

■ Each item of information contained in this information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.

☐ No item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned, having made reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this statement.

DEPOSIT ACCOUNT

■ Please charge any additional fees for the papers being filed herewith and for which no check or credit card payment is enclosed herewith, or credit any overpayment to deposit account number 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

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Form PTO 1449 (Modified) | U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

LIST OF REFERENCES CITED BY APPLICANT

IPE JC13 FEB 10 2005 PATENT & TRADEMARK OFFICE

ATTY DOCKET NO.	SERIAL NO.
219713US2	10/075,464
APPLICANT: Shigeru KAWANAKA	
FILING DATE: February 15, 2002	GROUP: 2826

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION YES	TRANSLATION NO
	AO	110488	09/18/1996	KOREA (corresponding to EP 0 551 214 A2)		X
	AP	0 551 214 A2	07/14/1993	EUROPE		
	AQ	5-110037	04/30/1993	JAPAN (with English Abstract)		X
	AR	WO 95/15562	06/08/1995	WIPO		
	AS					
	AT					
	AU					
	AV					

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)

	AW	Hideto HIDAKA, et al., "A High-Density Dual-Port Memory Cell Operation and Array Architecture for ULSI DRAM's", IEEE Journal of Solid-State Circuits, vol. 27, no. 4, April 1992, pages 610-617
	AX	
	AY	
	AZ	☐ Additional References sheet(s) attached

Examiner | Date Considered

*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.